

Scoped Types for Real-time Java

Tian Zhao

University of Wisconsin – Milwaukee

James Noble

Victoria University of Wellington

Jan Vitek

Purdue University

Abstract—A memory model based on scoped areas is one of the distinctive features of the Real-Time Specification for Java (RTSJ). Scoped Types ensure timely reclamation of memory and predictable performance. The price to pay for these benefits is an unfamiliar programming model that, at the same time, is complex, requires checking all memory accesses, and rewards design-time errors with run-time crashes. We investigate an alternative approach, referred to as *Scoped Types*, that simplifies the task of managing memory in real-time codes. The key feature of our proposal is that the run-time partition of memory imposed by scoped areas is straightforwardly mirrored in the program text. Thus cursory inspection of a program reveals which objects will inhabit the different scopes, significantly simplifying the task of understanding real-time Java programs. Moreover, we introduce a type system which ensures that no run-time errors due to memory access checks will occur. Thus a RTSJ-compliant virtual machine does not require memory access checks. The contributions of this paper are the concept of Scoped Types, and a proof soundness of the type system. Experimental results will be described in future work.

I. INTRODUCTION

The Real-Time Specification for Java (RTSJ) [1] is designed to allow the construction of large scale real-time systems in a type-safe programming language. The benefits of using Java for mission critical systems are currently being evaluated by, e.g., Boeing [2] and JPL [3]. As of this writing a high-quality commercial implementation of the specification has been released by TimeSys (JTime [4]) and an open source virtual machine is being developed at Purdue (Ovm [5]). While the RTSJ includes many necessary features, the one that is most likely to affect how real-time Java programs are written is the new memory management model based on *scoped memory areas*.

An obvious concern for meeting hard real-time constraints in Java is the interaction of automatic memory management with real-time tasks. While garbage collection (GC) frees the programmer from the burden of tracking memory usage, it introduces unpredictability because the timing and duration of GC pauses is unknown. To address this problem, the RTSJ provides: (a) regions of memory which are not subject to garbage collection, called *scoped memory areas*, or *scopes*, (b) threads that can not access the heap and thus can never interfere with, or be preempted by, the garbage collector. Scoped memory areas provide predictable allocation, and ensure that hard real-time threads will not block while memory is being reclaimed.

In principle, scoped memory is similar to a cactus-stack [6], [7]. Each scope provides a pool of memory that can be used to allocate objects. Individual objects cannot be deallocated,

instead the entire scope is reclaimed when its contents become unreachable. The main difference with stack allocation is that scopes are first-class entities which can be entered by multiple threads. The order in which threads enter scopes induces a runtime structure on scopes that determines permissible reference patterns. When a real-time thread executing in scope M_1 first enters scope M_2 , scope M_1 becomes the parent of M_2 . RTSJ semantics guarantees that M_2 will be reclaimed before M_1 , the lifetime of a nested scope is thus always shorter than that of its parent. Threads executing in an scope allocate objects from the same pool of memory and communicate through shared variables. When the last thread exits a scope, the objects allocated within it are reclaimed. The last-in first-out nature of scoped memory allows for objects allocated in nested scopes, e.g. M_2 , to refer objects in their parent, e.g. M_1 , but not the converse as holding on to a reference into a shorter-lived scope may lead to dangling references, and jeopardize type safety.

To ensure type safety of real-time Java programs, the following invariants must be maintained at runtime:

- 1) Because a scope can be reclaimed at any time, an outer scope may not hold a reference to an object within an inner scope.
- 2) To avoid cycles in the scope parent relation, the nesting structure of scopes is restricted to trees. In other words, a scope may have at most one parent.
- 3) Because scopes can be shared by multiple threads, objects allocated within a scope cannot be discarded until all threads have finished using that scope.

Maintaining these invariants impose burdens on programmers and increases the potential for bugs as they require reasoning about *localities*. With the RTSJ, programmers must be aware *where* an object has been allocated, a piece of information that cannot be obtained straightforwardly by inspection of the program text. The result is that scopes are, in our experience, the main source of program errors in RTSJ programs.

We propose a new programming mechanism called *Scoped Types*, designed to support safe scoped memory programming in concurrent real-time systems. Our goal is to devise a solution which does not require changes to the language or the tools that are used to write and run programs (e.g. compilers, development environment, and preprocessors) and ensure that programs will not experience run-time access violations. Yet Scoped Types should not impose undue restrictions on RTSJ programs. The underlying motivation for the proposed mechanisms is to keep the cost of adopting low. In fact, we have tried to retain the option of translating programs using

Scoped Types into RTSJ programs and running them on an unmodified real-time Java virtual machine.

To appeal to programmers, our proposal requires minimal syntactic overhead and only modest changes to RTSJ programs. With Scoped Types, the run-time hierarchy of scopes and subscopes is captured in the program text by the static hierarchy of Java packages. Thus, to comply with scoped types, RTSJ programs must be refactored so that objects that are meant to live in the same scope are declared in the same Java package. While this requires changes to grouping of classes and may entail increasing the visibility of some classes, these costs are mitigated by gains in clarity. The definition and behavior of scoped classes is constrained by six simple rules which taken together ensure that scope access errors will never occur, and object deallocation will never introduce dangling pointers. Furthermore, cycles in the scope hierarchy are impossible.

The contributions of this paper are the following:

- 1) **A new programming model** which provides *static correctness guarantees while remaining simple* and eschewing the need to modify Java in a significant way.
- 2) **Proof of soundness** by extracting the essence of scoped types in a core object calculus and proving soundness of the type system.

One of the difficulties in extending a language such as Java is that features interact. This can lead to undesirable side effects, even for seemingly simple changes, significantly complicating the lives of implementers and users. This motivates our choice of starting with a formalization of scoped types as a proof of soundness is an essential first step to any language extension of the kind we are considering here. We are currently implementing support for scoped types in the Ovm RTSJ virtual machine [5] and refactoring several large programs to abide with the scoped type discipline.

II. AN EXAMPLE: A REAL-TIME COLLISION DETECTOR

To illustrate the usage of scopes, we present the example of an aircraft collision detection algorithm [8]. Collision detection is performed by a single real-time thread which receives a series of *frames* containing aircraft call signs along with positions and direction vectors. The output of the algorithm is a warning each time any pair of aircraft are on a collision course. We have implemented two versions of the algorithm — one in plain Java and one in RTSJ. The Java implementation is 2500 lines of code of which fewer than 200 lines had to be adapted to make the program RTSJ compliant.

Collision detection is performed iteratively. A *Frame* object containing a number of *Aircraft* objects is received from the sensors once per iteration of the main run loop. The contents of the frame are used to update a state table and then compute collision vectors. The RTSJ implementation of the algorithm uses four scopes, the distinguished *ImmortalMemory* and *HeapMemory*, along with two user-defined instances of *ScopedMemory*. Fig. 1 illustrates the memory structure of the program. While the main object, *App*, of the application is created in immortal memory, none of the other objects should

be allocated there, as objects in immortal memory are never deleted. Thus, the first action of the `App.run()` method is to create and enter a new scope, `mem`. That scope will be used to contain the program’s stable storage. A second scope, `cdmem`, is used for temporary storage, so that all the temporary objects are deleted at the end of each iteration.

Fig. 2 illustrates the main points of the algorithm. The `App.main()` method is responsible for starting a new real-time thread (`App` extends `NoHeapRealtimeThread`). As real-time threads may not execute within the heap, the first action that is performed by that method is to enter immortal memory. Note the use of reflection (`newInstance`) to create objects in different scopes. Then the `App.run()` method creates a new scope to hold the application’s stable store (all state that must be preserved between iterations is stored in the instance of class `StateTable`) and starts an instance of `Runner` in the newly created scope.

The `Runner.run()` method is an example of the *scoped run loop* pattern [8]. The method starts by creating a scope, `cdmem`, to hold temporary objects. Then it repeatedly executes the `Detector.run()` method within `cdmem`. Since there is no other thread contending for that scope, after each iteration the scope is cleared. We remark that the `ScopedMemory` object itself remains intact between invocation, as does the `Detector` — both are allocated in the `mem` area which is not reclaimed for the lifetime of the application.

As can be seen from Fig. 2, although perhaps simple in theory, RTSJ Scoped Memory is complex in practice. For example, in each iteration of the run loop a new frame object is created along with an aircraft and a position. These objects are all allocated in the `cdmem` scope, whereas the state table is in the parent `mem` scope. In order to store a newly detected plane in the state table, the program has to reflectively create instances of `Aircraft` and `Position` in the correct (`mem`) scope. For another example, consider that

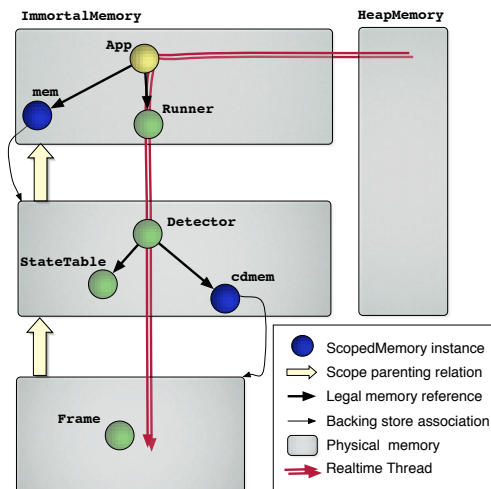


Fig. 1. Scopes in the example application. The main `App` object is allocated in immortal memory `imm`. Application stable state is held in the scoped area `mem`, per-iteration objects are allocated in `cdmem`.

```

class App extends NoHeapRealtimeThread {
    static void main() {
        imm = ImmortalMemory.instance();
        app = (App) imm.newInstance( App.class);
        app.start(); }

    void run() {
        LTMemory mem = new LTMemory( ...);
        mem.enter( new Runner() ); }
}

class Runner implements Runnable {
    void run() {
        LTMemory cdmem = new LTMemory( ...);
        Detector cd =
            new Detector( new StateTable() );
        while ( true )
            cdmem.enter( cd); }
}

class Detector implements Runnable {
    StateTable state;
    void run() {
        Frame frame = receiveFrame();
        Position pos_in_table =
            state.get( frame.getAircraft());
        if (pos_in_table == null) {
            mem = MemoryArea.getMemoryArea( this);
            Aircraft new_plane =
                mem.newInstance( Aircraft.class);
            frame.getAircraft().update( new_plane);
            pos_in_table =
                mem.newInstance( Position.class);
            state.put( new_plane, pos_in_table);
        }
        pos_in_table.update( frame.getPosition());
    }
}

```

Fig. 2. The main method of the application is used to bootstrap the real-time task. The run method of App is used to set up the application’s stable store. The Runner class holds the application’s main loop. All methods are public unless stated otherwise. Class LTMemory is a particular kind of scoped memory area which guarantees linear time allocation. Class NoHeapRealtimeThread is the parent class of all hard real-time thread classes, it is guaranteed not to interfere with the garbage collector.

the Aircraft.update() method (not shown here), takes an aircraft as argument and copies the information out of itself into its argument. We were forced to use this tortured design so that we can copy data allocated in the inner cdmem scope into an object in the out stable store mem scope.

Discussion. This complex explanation shows that a large amount of information about this example is implicit in the RTSJ code. The memory scopes within which variables are allocated (and therefore to which they can refer) are not recorded in the text of the program; there is no information about the scope that a particular instance of the Aircraft class is stored in, for example. This means that a minor typographical error could go undetected by the compiler, and then cause a runtime failure during rare circumstances as the program is run — such as when the program actually detects a collision. Similarly, the nesting relationship between the imm, mem, and cdmem scopes is implicit in the code: if the run() method of the Detector class attempted to reenter the mem scope, the program would suffer a ScopedCycleException. Finally, the programmer intends all objects contained within the cdmem to be discarded after each iteration, but this is not in any way obvious from the code of the program.

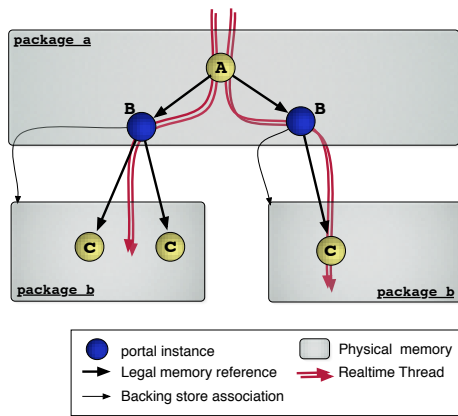
III. REAL-TIME PROGRAMMING WITH SCOPED TYPES

To make real-time Java programming more reliable and predictable, we have developed a type system that can statically guarantee the absence of reference assignment errors, enforce the single parent rule for scopes, and ensure that there can be no references into the contents of a scope when it is scheduled to be discarded. Our proposal is referred to as *Scoped Types*. Scoped Types require the addition of two modifiers to the

language, no compiler changes, and minimal support from the underlying virtual machine.

Language extensions. Our model distinguishes between two kinds of classes in a real-time Java program: *scoped classes* which are allocated within a particular memory scope, and *gate classes* which reify memory scopes. Most of the objects in the program are instances of scoped classes, and are allocated in memory scopes. Instances of gate classes turn scopes into first-class entities: threads enter memory scopes by invoking methods of gate objects, and exit scopes when these calls return. The key observation is that an object allocated in a scoped memory area can only be used in that scope and its nested subscopes. Thus, we statically restrict the accessibility of a scoped class to the classes whose instances are allocated in the same or nested scopes.

Integration with Java. To minimize the changes to the language, or at least to its syntax and to the tool processing the language, we take advantage of existing concepts, such as visibility rules and access modifiers, to integrate Scoped Types with Java. Scoped and gate classes are declared by appending the respective modifiers to class declarations (@scoped and @gate), no other annotations are needed. These annotations are consistent with the Metadata JSR, and will be recognized by Java 1.5 compilers. We call packages that contain scoped types *scoped packages*. Scoped packages are the unit of protection and of allocation. Each scoped package is the static representation of a family of memory scopes and defines the types of objects that may be allocated in these scopes. We use nested packages to represent potentially nested memory scopes: a memory scope created by some gate class in a scoped package can only contain nested subscopes defined by gate



```

package a;
@scoped class A {
    ...
}

package a.b;
@gate class B {
    ...
}
@scoped class C {
    ...
}

```

Fig. 3. A program written with Scoped Types. The program’s static structure consists of two packages a and a.b. At runtime two instance of the gate class B are created, thus giving rise to two distinct scopes. Notice that gate objects, like ScopedMemory instances in the RTSJ, are allocated in the parent scope. Overall, the code is shorter than the RTSJ version and makes explicit the allocation context of objects.

classes in immediate subpackages. Instances of classes defined in top-level package are allocated in immortal memory.

Dynamics. While scoped packages describe the static structure of an application, restricting programs to a single instance of each scope (and thus exactly matching the static package hierarchy) prevents some useful programming idioms. Thus, at runtime, every *instance* of a gate class corresponds to a new memory scope. So an application that creates two gates for the same package, gets two distinct scopes which can be used independently. The type system guarantees that references across sibling scopes cannot arise, thus objects allocated within two instances of the same gate class can safely be reclaimed at different times. A scope’s gate is the *only* object from the scoped package that is visible in the parent package. In fact, gates are allocated in their parent scope, just as RTSJ ScopedMemory objects are allocated in an enclosing area. The current allocation context is *always* defined by the package in which the current class was defined. Changing allocation context is thus as simple as calling a method of an object living in a different scope. Concurrency comes in quite naturally – multiple threads execute in the same scope if they invoke a method on the same gate. The implementation keeps track of the number of threads in a scope by a simple reference counting scheme. Just as in RTSJ, objects within a scope can be reclaimed when the last thread exits. Fig. 3 illustrates these concepts.

Static guarantees. Our model imposes some static constraints on the accessibility of classes. We require that scoped classes in a package be accessible only to the classes defined in that package and its subpackages, while gate classes are only accessible to classes defined in their immediate parent package. In other words, classes are *not* allowed to access classes in inner nested subpackages (other than the gates of their immediate subpackage). These constraints ensure that a package’s gate classes form an encapsulation boundary for classes outside that package: scoped classes, and classes in subpackages are inside

that encapsulation boundary. More importantly, they ensure that objects allocated in one scope may never have outgoing inferences to objects allocated in inner scopes, and thus that IllegalAssignmentErrors can never happen. Threads can only enter the scopes defined in some package (by calling methods on gate classes in that package) from the code in the immediate super-package. This ensures that the hierarchy of memory scopes always follows the same hierarchy as the corresponding packages, enforcing the single parent rule and preventing ScopedCycleExceptions.

Scoped Type Confinement Rules. Scoped Types’ static guarantees are enforced by the following syntactic rules that must hold for all scoped and gate classes. Rules C1, C2, and C3 bind scoped classes, while Rules S1 to S3 bind gate classes. Besides the visibility constraints of Rules C1 and S1, we also require that (C2) references of scoped type cannot be widened to types in other packages while (S2) the references of gate types cannot be widened to other types. Note that a reference of type C can be widened to type D only if C is a subtype of D. Reference widening can happen through operations such as assignments, casts, and method invocations. The restrictions on reference widening help us to track references by their

C1	A scoped type is visible only to classes in the same package or subpackages.
C2	A scoped type can only be widened to other scoped types in the same package.
C3	The methods invoked on a scoped type must be defined in the same package.
S1	A gate type is only visible to the classes in the immediate super-package.
S2	A gate type cannot be widened to other types.
S3	The methods invoked on a gate type must be defined in the same class.

static types.

These rules are similar in spirit to the confinement rules presented in [9]. The type system presented in the next section formalizes these intuitive rules.

Restrictions. Scoped Types do restrict the set of valid Java programs. Even though they do not require changes to the syntax they do change the programming model. To start with, while an instance of a scoped class may extend an arbitrary class, none of the methods defined outside of the scoped package can be invoked. This really means that, for scoped classes, inheritance of code from classes defined outside of their package is disallowed. Functionality provided in a (non-scoped) parent class must be overridden in the scoped class. Moreover the restrictions on widening mean that the reuse of library classes, such as vectors, hash tables and the like, will be limited. We defend these choices by remarking that most existing Java libraries have not been designed for a real-time setting. In our experience most library classes rely on garbage collection to reclaim internally allocated objects and are thus not suited for use in with the RTSJ. Another potentially contentious issue is that, from a software engineering point of view, grouping classes with respect to their allocation context may lead to somewhat unnatural program structures. While this may be true, we believe that the benefits in clarity and correctness are sufficient to justify our approach. We discuss possible extensions to overcome some of these restrictions in Section VI.

A. The Example Revisited

Scoped Types simplify programming within nested memory scopes. We can rewrite the collision detector example to use Scoped Types with very few changes. We first need to define three packages to model the three scopes of the original application. This is because in Scoped Types, the programmers have to choose the package within which each class should be

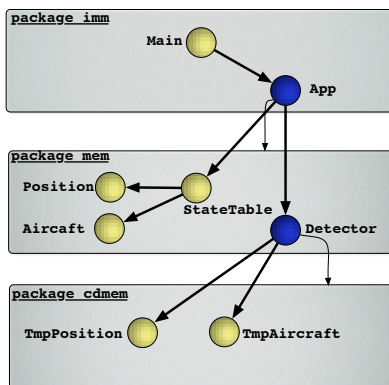


Fig. 4. The reference patterns of scoped and gate objects in the Scoped Type version of the example. The only references allowed to go from a parent package to a child are references originating from the gate. The gate object is a dominator for all scoped types in its package and subpackages. Note that although the figure does not show it, references from child packages to their parents are allowed.

statically *defined*, rather than deciding where instance should be dynamically *allocated*, as in RTSJ.

The scoped version of the program, shown in Fig. 4 and the code in Fig. 5, consists of three packages, `imm`, `imm.mem` and `imm.mem.cdmem` mirroring the dynamic scope hierarchy of the algorithm. The class `Main` is the only class that executes in immortal memory, and its only purpose is to create an instance of the `App` class, which is the gate of the `imm.mem` package. The `App` class, once started, will then allocate an instance of the `Detector` class, which is the gate for the `imm.mem.cdmem` scope. The run loop again boils down to calling the detector’s `run` method. The program’s stable state is held in the `imm.mem` package, and is composed of instances of the `StateTable`, `Aircraft`, and `Position` classes. Per-iteration temporary objects are stored in the `cdmem` package

```

package imm;

@scoped class Main {
    static void main() { new App().start();
    }
}

package imm.mem;

@gate final class App
    extends NoHeapRealtimeThread {
    void run() {
        cd = new Detector();
        state = new StateTable();
        key = new Aircraft();
        while ( true ) cd.run( state, key);
    }
}

@scoped class StateTable ...
@scoped class Aircraft ...
@scoped class Position ...

package imm.mem.cdmem;

@gate final class Detector {
    void run(StateTable state, Aircraft key){
        Frame frame = receiveFrame();
        TmpAircraft plane = frame.getAircraft();
        plane.update( key);
        Position pos_in_table = state.get( key);
        if ( pos_in_table == null )
            state.put(plane.copy(),
                frame.getPosition().copy());
        else
            frame.getPosition()
                .update(pos_in_table);
    }
}

@scoped class TmpAircraft ...
@scoped class TmpPosition ...
@scoped class Frame ...

```

Fig. 5. The collision detector example with Scoped Types. The program is split into three packages representing the different scopes used in that program. All support classes (e.g. `Aircraft`) are defined in the appropriate scope.

and consist of `TmpAircraft`, `TmpPosition` and `Frame`.

Notice that with `Scoped Types` it is impossible to confuse planes in the inner `imm.mem.cdmem` scope with planes in the stable `imm.mem` scope, as they are represented by different types. A `copy` method is implemented in `TmpAircraft` to create a `Aircraft` object that *must*, by definition, be allocated in the parent scope. Similarly, since the state table is allocated in `imm.mem`, the types in `imm.mem.cdmem` are not accessible to it. Thus, we cannot use a `TmpAircraft` object as the key to find out whether a plane is already stored in the table, and we use an `Aircraft` object instead. The `update` method of a `TmpAircraft` object refreshes the `key` with the information about the current plane.

In this way, `Scoped Types` statically maintains the invariants that `RTSJ` checks dynamically. By statically associating scoped objects to their defining packages, `Scoped Types` can ensure that illegal incoming references are never created. Similarly, by modeling nested scopes with nested packages, `Scoped Types` ensure that scopes will never form cycles. Finally, by statically tracking the objects contained within each scope, `Scoped Types` ensure that it is safe to discard all the objects in a scope once the last thread has left it.

B. Refactoring an existing system

We are in the process of refactoring a `RTSJ` program called `Zen` to use `Scoped Types`. `Zen` is a `CORBA` object request broker designed to support distributed, real-time and embedded (`DRE`) applications, written in the `Real-time Specification for Java` [10]. `Zen` has been designed for memory-constrained `DRE` applications. For our experiment we have selected a minimal configuration (about 20K `LOC` of `Java` code) that provides sufficient functionality for a number of benchmark applications that will be tested on a 300Mhz `PPC` board with 64MB of memory. We used the `Eclipse` development environment to assist in the refactoring exercise.

The process of refactoring `Zen` proved surprisingly simple. The first step is to instrument the program to print for each object instantiated its class and the scope in which it is created and also to print the parent relation between scopes. The second step involve creating `Java` packages that mirrored the scope hierarchy. Then, classes that are used unambiguously in only one scope can be moved to the respective package (with `Eclipse` this is painless). The remaining classes are used in several scopes, for these it may be necessary to either find a way to modify the application logic, or, in last resort, to duplicate code.

Figure 6 summarizes the difference in package structure between `Zen` and `ScopedZen`. The majority of classes in subpackages of `zen.orb` are moved up to the new package `orb`. This corresponds to the main memory area in which an `ORB` executes. The class `ORB` is the gate class for the `orb` package, each instance of this class represent one `ORB` running in the virtual machine. The `orb.transport` and `orb.transport.message` are used in the processing of requests. Once a request has been processed all objects allocated in those scopes are reclaimed. Finally, the other scopes

ZEN	SCOPED ZEN
zen.orb	orb
zen.orb.any	orb.acceptor
zen.orb.dynany	orb.poa
zen.orb.giop	orb.transport
zen.orb.policies	orb.transport.message
zen.orb.resolvers	orb.threadpool
zen.orb.transport	orb.waiter
zen.orb.transport.iiop	
zen.poa	
zen.poa.mechanism	
zen.poa.policy	

Fig. 6. Refactoring the `Zen` `CORBA` `ORB`.

(`orb.acceptor`, `orb.threadpool` and `orb.waiter`) are present to mirror the original design of `Zen`. They could be folded in `orb` as their lifetime is exactly that of `orb`.

IV. THE SJ CALCULUS

To gain confidence in the programming model underlying our proposal, we introduce the `SJ` calculus, a sparse imperative and concurrent object calculus, modeled after `Featherweight Java` [11], in which scopes are first-class values. `SJ` formalizes the type confinement rules of `Scoped Type` in terms of a type system. Our proof of type soundness gives us the guarantee that confinement cannot be breached during execution of a well-typed program. We can then proceed to prove that the shape of the scope hierarchy is restricted to tree. And, finally, that deallocation of a scope will not result in dangling references. `SJ` is a simple object calculus, to keep the semantics concise we have omitted some features that are not essential to the main results. These features include static methods, synchronization, access modifiers, and down-cast expressions. Some specific features related to scoped memory such as the size and the type of the memory area (linear or variable allocation time) are also omitted.

A. Syntax and Types

The syntax of the `SJ` calculus, Figure 7, draws on our previous work [9]. The formalism and syntax is based on the `Featherweight Java` (`FJ`) system which has been widely adopted as a vehicle of language research. `SJ` has two kinds of class declarations, *scoped classes* and *gate classes*, the former annotated with a `scoped` and the latter with a `gate`. Classes belong to packages, which can be nested in an arbitrary

$$\begin{aligned}
 L & ::= \circ \text{ class } P.C \triangleleft D \{ \bar{C} \bar{f}; K \bar{M} \} \\
 K & ::= C() \{ \text{super}(); \text{this}.\bar{f} := \overline{\text{new } D}(); \} \\
 M & ::= C \text{ m}(\bar{C} \bar{x}) \{ \text{return } e; \} \\
 e & ::= x \mid e.f \mid e.m(\bar{e}) \mid \text{new } C() \mid e.f := e \\
 & \quad \mid \text{spawn } e \mid \text{reset } e \mid v \\
 \circ & ::= \text{gate} \mid \text{scoped} \quad v ::= \ell \quad P ::= p \mid p.P
 \end{aligned}$$

Fig. 7. Syntax of the `SJ` calculus.

Allocation:

Suppose $\sigma(\ell) = C_0^{\ell'}(\bar{v})$
 $allocScope_\sigma(C, \ell) = \ell$ if C_0 is a gate type
and $\left\{ \begin{array}{l} \text{either } C \text{ is a scoped type and} \\ \quad C, C_0 \text{ in the same package} \\ \text{or } C \text{ is a gate type and} \\ \quad C \text{ in the immediate subpackage of } C_0 \end{array} \right.$
 $allocScope_\sigma(C, \ell) = allocScope_\sigma(C, \ell')$ otherwise

Evaluation context:

$E[o] ::= \circ \mid E[o].m(\bar{e}) \mid v.m(\dots, v_{i-1}, E[o], e_{i+1} \dots)$
 $\mid E[o].f_i \mid E[o].f_i := e \mid v.f_i := E[o] \mid \text{reset } E[o]$

Scope reference counts:

$refcount(\ell, t[\bar{\ell}e] \mid P') = count_\ell(\bar{\ell}) + refcount(\ell, P')$
 $refcount(\ell, \emptyset) = 0 \quad count_\ell(\emptyset) = 0$
 $count_\ell(\bar{\ell}. \ell) = 1 + count_\ell(\bar{\ell}) \quad count_\ell(\bar{\ell}. \ell') = count_\ell(\bar{\ell})$

Fig. 8. Auxiliary definitions.

package hierarchy. Each package may contain a mixture of scoped and gate classes. We add an assignment expression and an expression for creating a new thread of control. Finally, we add a *reset* expression, which clears the fields of a gate object if the objects is not used by any threads. Resetting a gate corresponds to deallocating a scope in RTSJ, the operation was added to model GC, but is interesting in its own right as we observed in [8].

We take metavariables C, D to range over classes, M to range over methods, K over constructors, and f and x to range over fields and variables (including parameters and the pseudo variable `this`), respectively. We also use P for package names, e for expressions and ℓ for memory references. We use over-bar to represent a finite ordered sequence, for instance, \bar{f} represents $f_1 f_2 \dots f_n$. The term $\bar{1}.\bar{1}$ denotes sequence concatenation. The calculus has a call-by-value semantics. The expression $[\bar{v}/v_i]\bar{v}$ yields a sequence identical to \bar{v} except in the i th field which is set to v . We use the usual dot notation to represent nested packages. That is, the package $p.q$ is a subpackage of p . The presentation of the calculus inherits some of the syntactic oddities of FJ, so $\bar{e}e$ is a short hand for $e_1 \dots e_n e$, and $m(\bar{C}\bar{x})$ stands for $m(C_1 x_1, \dots, C_n x_n)$.

B. Semantics

In SJ, each gate object represents a distinct scoped memory area and whenever a gate is reset all of the objects that were allocated within the associated scope are reclaimed. While the package hierarchy imposes a static structure on scopes, gate objects allow multiple scope instances to be created at runtime. The main restriction imposed by SJ is that a gate can only allocate objects of scoped classes belonging to the same package and gates defined in immediate subpackages. When this restriction is combined with confinement invariants that prevents gate objects leaking from their parent package, we obtain the key property for scoped memory management,

namely the restriction that threads enter scopes in the same order as the nesting relation of the packages containing the gate classes.

As in Featherweight Java, the semantics assumes the existence of a class table containing the definitions of all classes. We had to add a store σ and a collection of threads \bar{P} labeled by distinct identifiers \bar{t} . Objects are of the form $C^\ell(\bar{v})$, where C is a class, \bar{v} the value of the fields, and ℓ the gate of the scope in which it was allocated. The store σ is a sequence, $C^\ell(\bar{v})$, of objects, each denoted by a distinct label ℓ_i . Fig. 8 defines a number of auxiliaries relations. The partial function $allocScope_\sigma(C, \ell)$ retrieves the allocation scope for an object of the type C when the current receiver object is ℓ . Our type system ensures that all the scopes form a tree. Intuitively, $allocScope_\sigma(C, \ell)$ searches the scope tree upward starting from ℓ or the scope of ℓ until it finds a scope ℓ' of the type C' , which is in the same package as C if C is scoped and is in the immediate super-package of C if C is a gate. For some C and ℓ , $allocScope_\sigma(C, \ell)$ is not defined. An evaluation context, Fig. 8, is an expression $E[o]$ with a hole that can be filled in with another expression of proper type. An expression e_0 is written as $E[e]$ only if e is in the form of v , $(C) v$, $v.f$, $v.f := v'$, $v.m(\bar{v})$, `spawn e`, or `reset e`. For a non-value expression e_0 , there exists an unique evaluation context $E[o]$ such that $e_0 = E[e]$ and e is not a value. Evaluation contexts do not include the body of `spawn`.

The dynamics semantics of SJ is split in two: expression evaluation rules given in Fig. 9 and the computation rules in Fig. 10.

Expression rules. These evaluation rules consider only operations performed within a single thread. The evaluation relation has the form $\sigma, \ell e \rightarrow \sigma', \ell' e'$ where σ is the initial store, ℓ is the reference to object currently executing, and e the expression to evaluate. The reduction rules field select (R-FIELD), field update (R-UPDATE), and method invocation (R-INVK) are not surprising, where $mbody(m, C)$ returns parameters \bar{x} and method body e of m when it is invoked on an object of the type C . The instantiation rule (R-NEW) must ensure that the class of the object about to be created, C , can be instantiated in the current scope (as defined by $allocScope_\sigma(C, \ell_0) = \ell'$). The fields of C must also be initialized, though not in the current scope but rather in the scope of the newly allocated object (this only matters if C is a gate). Finally the store is updated with a fresh reference ℓ bound to the newly allocated object. The helper function $init(C)$ returns the list of initial values of the fields in C (including the fields inherited from its super-classes). Rule (R-RESET) clears all fields of the target object. This ensures that all objects previously in the scope are now unreachable. In practice, programmers do not explicitly write such reset expressions, they are performed implicitly by the VM.

Computation rules. The computation rules are of the form $\sigma, P \Rightarrow \sigma', P'$ where σ is a store and P is a set of threads. Each thread $t[\bar{\ell}e]$ in P has a distinct label t and a runtime call

$$\begin{array}{c}
\frac{\sigma(\ell) = \mathbf{C}^{\ell'}(\bar{v}) \quad \mathit{fields}(\mathbf{C}) = (\bar{\mathbf{C}} \bar{\mathbf{f}})}{\sigma, \ell_0 \ell.\mathbf{f}_i \rightarrow \sigma, \ell_0 v_i} \quad (\text{R-FIELD}) \\
\\
\frac{\sigma(\ell) = \mathbf{C}^{\ell'}(\bar{v}) \quad \mathit{fields}(\mathbf{C}) = (\bar{\mathbf{C}} \bar{\mathbf{f}})}{\sigma' = \sigma[\ell \rightarrow \mathbf{C}^{\ell'}([\bar{v}/v_i \bar{v}])]} \quad (\text{R-UPDATE}) \\
\frac{\sigma, \ell_0 \ell.\mathbf{f}_i := v \rightarrow \sigma', \ell_0 v}{} \\
\\
\frac{\begin{array}{l} \mathit{allocScope}_\sigma(\mathbf{C}, \ell_0) = \ell' \quad \ell \text{ fresh} \\ \mathit{init}(\mathbf{C}) = \mathbf{new D}() \quad \sigma'' = \sigma[\ell \rightarrow \mathbf{C}^{\ell'}(\mathbf{null})] \\ \sigma'', \ell \mathbf{new D}_1() \rightarrow \sigma_1, \ell v_1 \\ \dots \\ \sigma_{n-1}, \ell \mathbf{new D}_n() \rightarrow \sigma_n, \ell v_n \\ \sigma' = \sigma_n[\ell \rightarrow \mathbf{C}^{\ell'}(\bar{v})] \end{array}}{\sigma, \ell_0 \mathbf{new C}() \rightarrow \sigma', \ell_0 \ell} \quad (\text{R-NEW}) \\
\\
\frac{\begin{array}{l} \sigma(\ell) = \mathbf{C}^{\ell'}(\bar{v}) \quad \mathit{init}(\mathbf{C}) = \mathbf{new D}() \\ \sigma, \ell \mathbf{new D}_1() \rightarrow \sigma_1, \ell v'_1 \\ \dots \\ \sigma_{n-1}, \ell \mathbf{new D}_n() \rightarrow \sigma_n, \ell v'_n \\ \sigma' = \sigma_n[\ell \rightarrow \mathbf{C}^{\ell'}(\bar{v}')] \end{array}}{\sigma, \ell_0 \mathbf{reset } \ell \rightarrow \sigma', \ell_0 \ell} \quad (\text{R-RESET}) \\
\\
\frac{\sigma(\ell) = \mathbf{C}^{\ell'}(\bar{v}) \quad \mathit{mbody}(\mathbf{m}, \mathbf{C}) = (\bar{x}, \mathbf{e})}{\sigma, \ell_0 \ell.\mathbf{m}(\bar{v}) \rightarrow \sigma, \ell [\bar{v}/\bar{x}, \ell'/\mathit{this}]\mathbf{e}} \quad (\text{R-INVK})
\end{array}$$

Fig. 9. Expression evaluation.

stack which is a list of receiver-expression pairs ℓ, \mathbf{e} . Rule (G-STEP) is simple, it picks one thread for execution and evaluates the expression $E[\mathbf{e}]$ on the top of the thread's stack. Note that this rule applies when \mathbf{e} is not a method invocation or reset expression. Rule (G-ENTER) evaluates a thread $t[\bar{\ell}\mathbf{e}.\ell E[\mathbf{e}]]$ containing a method call $\mathbf{e} = E[\mathbf{e}_0]$ and $\mathbf{e}_0 = \ell'.\mathbf{m}(\bar{v})$. It creates a new stack frame for the body of the method, \mathbf{e}' , and the result is a frame $\bar{\ell}\mathbf{e}.\ell E[\mathbf{e}_0].\ell' \mathbf{e}'$. If the expression on the top of a thread's stack is reduced to a value v , then by Rule (G-RETURN), the thread can pop the stack frame and continue execution with v as the resulted value of a method call. Note that if the expression replaced by v is not a value, then its evaluation context is unique. Thus, the replacement is unambiguous. Rule (G-SPAWN) evaluates a thread $t[\bar{\ell}\mathbf{e}.\ell E[\mathbf{e}]]$ containing a spawn expression $\mathbf{e} = E[\mathbf{spawn } \mathbf{e}_0]$. The value of the spawn expression in \mathbf{e} is the distinguished ℓ^{th} which is a unique, global reference to an object of class `Thread` and we assume that ℓ^{th} is allocated in immortal memory. A new thread t' is created to evaluate $\ell \mathbf{e}_0$. The new thread is started with a call stack $\bar{\ell}\ell^{\text{th}}$ that matches the call stack of the original thread t to ensure that scope reference counts are accurate. Rule (G-RESET) clears the fields of a gate ℓ' when no thread is using that gate (*i.e.* when $\mathit{refcount}(\ell', P) = 0$). For simplicity, the fields of a gate are reset to default value explicitly by a `reset` expression of the form `reset` ℓ' and if the reference count of ℓ' is not zero, then the fields of ℓ' are

$$\begin{array}{c}
\frac{\begin{array}{l} P = P'' \mid t[\bar{\ell}\mathbf{e}.\ell E[\mathbf{e}]] \\ P' = P'' \mid t[\bar{\ell}\mathbf{e}.\ell E[\mathbf{e}']] \\ \mathbf{e} \neq \mathbf{reset } \ell', \ell'.\mathbf{m}(\bar{v}) \quad \sigma, \ell \mathbf{e} \rightarrow \sigma', \ell \mathbf{e}' \end{array}}{\sigma, P \Rightarrow \sigma', P'} \quad (\text{G-STEP}) \\
\\
\frac{\begin{array}{l} P = P'' \mid t[\bar{\ell}\mathbf{e}.\ell E[\mathbf{e}_0]] \\ P' = P'' \mid t[\bar{\ell}\mathbf{e}.\ell E[\mathbf{e}_0].\ell' \mathbf{e}'] \\ \mathbf{e}_0 = \ell'.\mathbf{m}(\bar{v}) \quad \sigma, \ell \mathbf{e}_0 \rightarrow \sigma, \ell' \mathbf{e}' \end{array}}{\sigma, P \Rightarrow \sigma, P'} \quad (\text{G-ENTER}) \\
\\
\frac{\begin{array}{l} P = P'' \mid t[\bar{\ell}\mathbf{e}.\ell E[\mathbf{e}]].\ell' v] \\ P' = P'' \mid t[\bar{\ell}\mathbf{e}.\ell E[v]] \\ E[\mathbf{e}] = \ell^{\text{th}} \vee \mathbf{e} \text{ is not a value} \end{array}}{\sigma, P \Rightarrow \sigma, P'} \quad (\text{G-RETURN}) \\
\\
\frac{\begin{array}{l} P = P'' \mid t[\bar{\ell}\mathbf{e}.\ell E[\mathbf{e}]] \\ P' = P'' \mid t[\bar{\ell}\mathbf{e}.\ell E[\ell^{\text{th}}]] \mid t'[\bar{\ell}\ell^{\text{th}}.\ell \mathbf{e}'] \\ \mathbf{e} = E[\mathbf{spawn } \mathbf{e}'] \quad t' \text{ fresh} \end{array}}{\sigma, P \Rightarrow \sigma, P'} \quad (\text{G-SPAWN}) \\
\\
\frac{\begin{array}{l} P = P'' \mid t[\bar{\ell}\mathbf{e}.\ell E[\mathbf{reset } \ell']] \\ P' = P'' \mid t[\bar{\ell}\mathbf{e}.\ell E[\ell']] \\ \text{if } \mathit{refcount}(\ell', P) \neq 0 \text{ then } \sigma' = \sigma \\ \text{else } \sigma, \ell \mathbf{reset } \ell' \rightarrow \sigma', \ell \ell' \end{array}}{\sigma, P \Rightarrow \sigma', P'} \quad (\text{G-RESET})
\end{array}$$

Fig. 10. Computation rules.

not cleared (this makes `reset` nonblocking to avoid deadlock).

C. Type Rules

The typing rules are shown in Figure 11 and 12. Some auxiliary functions used in typing rules are defined as follows: $\mathit{fields}(\mathbf{C})$ returns the list of field declarations in the class \mathbf{C} (including the inherited fields) in the form of $\bar{\mathbf{C}} \bar{\mathbf{f}}$; $\mathit{mdef}(\mathbf{m}, \mathbf{C})$ returns the defining class of the method \mathbf{m} by searching the class hierarchy upward from \mathbf{C} ; $\mathit{mtype}(\mathbf{m}, \mathbf{C})$ returns the type signature $\bar{\mathbf{C}} \rightarrow \mathbf{C}'$ of the method \mathbf{m} called on the type \mathbf{C} , where $\bar{\mathbf{C}}, \mathbf{C}'$ are the parameter and return types. The type judgments are of the form $\Gamma, \Sigma \vdash \mathbf{e} : \mathbf{C}$, where Γ is the type environment of variables and Σ is the type environment of object labels.

The subtyping relation $<:$ is a reflexive and transitive closure of the relation that $\mathbf{C} <: \mathbf{C}'$ if the class \mathbf{C} extends the class \mathbf{C}' . We define the partial order \preceq on types to limit the variables that can refer to scoped objects and gates; $\mathbf{C} \preceq \mathbf{C}'$ is defined if $\mathbf{C} <: \mathbf{C}'$, and \mathbf{C}, \mathbf{C}' are both scoped types in the same package or \mathbf{C} is a gate type and $\mathbf{C} = \mathbf{C}'$. If $\mathbf{C} \preceq \mathbf{C}'$, then we say that \mathbf{C} is a *scope-safe* subtype of \mathbf{C}' and the widening of a reference from the type \mathbf{C} to \mathbf{C}' is *scope-safe*.

By Rules (T-UPDATE) and (T-INVK), the reference widening in the field assignments and parameters passing is *scope-safe*. Rule (T-STORE) of the form $\Sigma \vdash \sigma$ says that object store σ is well typed, if the type environment Σ has the same domain as σ and for each object label ℓ in the domain of σ , $\Sigma(\ell)$ is equal to the type of $\sigma(\ell)$ and $\sigma(\ell)$ must also be well-typed. If

$\Gamma, \Sigma \vdash \mathbf{x} : \Gamma(\mathbf{x})$	(T-VAR)
$\Gamma, \Sigma \vdash \ell : \Sigma(\ell)$	(T-LOC)
$\frac{\Gamma, \Sigma \vdash \mathbf{e}_0 : \mathbf{C} \quad \text{fields}(\mathbf{C}) = (\bar{\mathbf{C}} \bar{\mathbf{f}})}{\Gamma, \Sigma \vdash \mathbf{e}_0.f_i : \mathbf{C}_i}$	(T-FIELD)
$\frac{\Gamma, \Sigma \vdash \mathbf{e}_0 : \mathbf{C}_0 \quad \text{mdef}(\mathbf{m}, \mathbf{C}_0) = \mathbf{C}'_0 \quad \text{mtype}(\mathbf{m}, \mathbf{C}'_0) = \bar{\mathbf{C}} \rightarrow \mathbf{C} \quad \Gamma, \Sigma \vdash \bar{\mathbf{e}} : \bar{\mathbf{D}} \quad \bar{\mathbf{D}} \preceq \bar{\mathbf{C}} \quad \mathbf{C}_0 \preceq \mathbf{C}'_0}{\Gamma, \Sigma \vdash \mathbf{e}_0.m(\bar{\mathbf{e}}) : \mathbf{C}}$	(T-INVK)
$\Gamma, \Sigma \vdash \text{new } \mathbf{C}() : \mathbf{C}$	(T-NEW)
$\frac{\Gamma, \Sigma \vdash \mathbf{e}_0 : \mathbf{C}_0 \quad \text{fields}(\mathbf{C}_0) = (\bar{\mathbf{C}} \bar{\mathbf{f}}) \quad \Gamma, \Sigma \vdash \mathbf{e} : \mathbf{C} \quad \mathbf{C} \preceq \mathbf{C}_i}{\Gamma, \Sigma \vdash \mathbf{e}_0.f_i = \mathbf{e} : \mathbf{C}_i}$	(T-UPDATE)
$\frac{\Gamma, \Sigma \vdash \mathbf{e} : \text{Thread}}{\Gamma, \Sigma \vdash \text{spawn } \mathbf{e} : \text{Thread}}$	(T-SPAWN)
$\frac{\Gamma, \Sigma \vdash \mathbf{e} : \mathbf{C} \quad \mathbf{C} \text{ is a gate}}{\Gamma, \Sigma \vdash \text{reset } \mathbf{e} : \mathbf{C}}$	(T-RESET)

Fig. 11. Expression typing.

$\sigma(\ell) = \mathbf{C}^{\ell'}(\bar{\mathbf{v}})$, then by Rule (T-STORELOC), an object $\mathbf{C}^{\ell'}(\bar{\mathbf{v}})$ is well-typed, if the types of $\bar{\mathbf{v}}$ are *scope-safe* subtypes of the field types.

In the typing rule for class (T-CLASS), we require that in a class \mathbf{C} , and the types of the fields and the base class must be visible in \mathbf{C} . Also, all methods in a class must be well-typed by Rule (T-METHOD). If a method in the class \mathbf{C}_0 is well-typed, then the method body \mathbf{e} is well-typed by the expression typing rules, the type of the method body is a *scope-safe* subtype of the return type, and in addition, the method body must be visible in \mathbf{C}_0 as defined by the judgment $\Gamma \vdash \text{visible}(\mathbf{e}, \mathbf{C}_0)$. The predicate $\text{override}(\mathbf{m}, \mathbf{C}_0, \bar{\mathbf{C}} \rightarrow \mathbf{C})$ in Rule (T-METHOD) is true if either the method \mathbf{m} is not accessible in \mathbf{C}_0 or the type signature returned by $\text{mdef}(\mathbf{m}, \mathbf{C}_0)$ is the same as $\bar{\mathbf{C}} \rightarrow \mathbf{C}$. Note that in (T-CLASS) we abuse notation by writing $\text{visible}(\bar{\mathbf{C}}, \mathbf{C})$ to assert that all types in the $\bar{\mathbf{C}}$ are visible in \mathbf{C} .

Visibility of types and expressions. The static constraints in our model are mostly to restrict widening of references, and also to limit the accessibility of expressions by their types. For example, an expression of scoped type \mathbf{C} is only visible in the defining package of \mathbf{C} and its subpackages. We define a relation on types – $\text{visible}(\mathbf{C}, \mathbf{C}_0)$ (type \mathbf{C} is *visible from* type \mathbf{C}_0), which encodes the SJ access control rules: a scoped type defined in package \mathbf{P} is visible to the class \mathbf{C}_0 defined in \mathbf{P} and its subpackages; a gate class \mathbf{C} is only visible from the class \mathbf{C}_0 defined in the immediate parent package. One slightly surprising implication of this definition is that a gate type is not visible in its own class definition. Thus a gate class \mathbf{C} does not contain code that refers to itself with the exception, as we shall see later, of the pseudo variable `this` which may indeed be used to access fields and methods from within the

Store Typing:

$\frac{\text{dom}(\Sigma) = \text{dom}(\sigma) \quad \forall \ell \in \text{dom}(\sigma) . \Sigma \vdash \sigma(\ell) \wedge \Sigma(\ell) = \mathbf{C} \text{ if } \sigma(\ell) = \mathbf{C}^{\ell_0}(\bar{\mathbf{v}})}{\Sigma \vdash \sigma}$	(T-STORE)
$\frac{\text{fields}(\mathbf{C}) = (\bar{\mathbf{C}} \bar{\mathbf{f}}) \quad \emptyset, \Sigma \vdash \bar{\mathbf{v}} : \bar{\mathbf{D}} \quad \bar{\mathbf{D}} \preceq \bar{\mathbf{C}}}{\Sigma \vdash \mathbf{C}^{\ell}(\bar{\mathbf{v}})}$	(T-STORELOC)

Method typing:

$\frac{\Gamma = \bar{\mathbf{x}} : \bar{\mathbf{C}}, \text{this} : \mathbf{C}_0 \quad \Gamma, \emptyset \vdash \mathbf{e} : \mathbf{C}' \quad \mathbf{C}' \preceq \mathbf{C} \quad \text{override}(\mathbf{m}, \mathbf{D}, \bar{\mathbf{C}} \rightarrow \mathbf{C}) \quad \Gamma \vdash \text{visible}(\mathbf{e}, \mathbf{C})}{\mathbf{C} \text{ m}(\bar{\mathbf{C}} \bar{\mathbf{x}}) \{ \text{return } \mathbf{e}; \} \text{ OK IN } \mathbf{C}_0 \triangleleft \mathbf{D}}$	(T-METHOD)
---	------------

Class typing:

$\frac{\mathbf{K} = \mathbf{C}() \{ \text{super}(); \text{this}.\bar{\mathbf{f}} := \text{new } \bar{\mathbf{D}}(); \} \quad \bar{\mathbf{M}} \text{ OK IN } \mathbf{C} \triangleleft \mathbf{D} \quad \bar{\mathbf{D}} \preceq \bar{\mathbf{C}} \quad \text{visible}(\bar{\mathbf{C}}\bar{\mathbf{D}}, \mathbf{C})}{\text{class } \mathbf{P}.\mathbf{C} \triangleleft \mathbf{D} \{ \bar{\mathbf{C}} \bar{\mathbf{f}}; \mathbf{K} \bar{\mathbf{M}} \} \text{ OK}}$	(T-CLASS)
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Fig. 12. Type rules of store, method, and class.

gate class.

We check the method body to determine whether type visibility constraints are violated in a class. In Rule (T-METHOD), the judgment $\Gamma \vdash \text{visible}(\mathbf{e}, \mathbf{C}_0)$ holds if \mathbf{e} of type \mathbf{C} is visible in a class \mathbf{C}_0 , which means that either $\mathbf{e} = \text{this}$ or the type \mathbf{C} is visible in the class \mathbf{C}_0 (i.e. $\text{visible}(\mathbf{C}, \mathbf{C}_0)$) and all the subexpressions of \mathbf{e} are visible in \mathbf{C}_0 . We make an exception for `this` because even though a gate type is visible only to the classes of its immediate super-package, a gate object must be able to use the variable `this` for accessing its fields and calling its methods. For any scoped class, the type of the variable `this` are always visible in its class.

D. Properties.

The purpose of our model is to simplify the allocation of objects in scoped memory areas. Thus, we would like to statically guarantee the properties that during the evaluation of a real-time program,

- 1) the nesting structure of scopes remain a tree,
- 2) deallocated objects in scopes are no longer accessible.

In RTSJ, the nesting structure of scopes is determined by how threads enter scopes. In our model, the scope structure is fixed by how the gate objects representing the scopes are created. That is, if a scope a is represented by a gate object created in the scope b , then a must be directly contained in b ; moreover, the gate object representing a is defined in the immediate subpackage of the gate object representing b . Thus, our type system guarantees that the scopes represented by the gate objects always form a tree. It also ensures that the threads in a program will preserve such a scope tree such that each thread either enters the scopes already entered by the thread or enters a new scope directly contained in the current scope of

the thread. Thus, even though a scope stack of a thread may grow indefinitely (e.g. the thread reenters the scopes already on stack), the nesting structure of scopes resembles the nesting structure of the scoped packages and always remains a tree.

To ensure that deallocated objects are no longer accessible, we require that a scoped object can only access objects with the same or longer lifetime, while a gate object can in addition access the objects allocated in the scope that it represents. Thus, when the last thread in a scope exits, the objects in that scope can be deallocated. The deallocated objects are no longer accessible in the program because they are only accessible to the scoped objects with the same or shorter lifetime and to the gate of the scope, but those scoped objects are already deallocated and no method of the gate object is being invoked. The above accessibility constraints are enforced by SJ's type rules, where a scoped type is accessible in the classes of its defining package and the subpackages. It is possible for two instances of the same class to be allocated in two sibling scopes (they share some parent scope). To prevent such objects from accessing each other, we limit the access to a gate object to itself and the classes in its immediate super-package. Consequently, an object may only gain access to the gate of its own scope and the gates of its immediate nested scopes and thus, it cannot reference objects in its sibling scope.

Our proof strategy for the above properties is to show that the safety invariant that we define below is preserved in each reduction step. We say that an object o can *safely access* o' if either o' has longer lifetime than o or o is the gate of the scope where o' is allocated. A program σ, P is *safe* if for each label ℓ defined in σ , the object $\sigma(\ell)$ can *safely access* the objects referenced in its fields, and for each frame $\ell \mathbf{e}$ in the call stack of each thread in P , the object $\sigma(\ell)$ can *safely access* every object referenced in \mathbf{e} .

A program σ, P is well-typed if it is *safe* and $\exists \Sigma$ such that $\Sigma \vdash \sigma$ and the call stack of each thread in P is well-typed.

Given Σ , the call stack $\overline{\ell \mathbf{e}}. \ell E[\mathbf{e}]. \ell' \mathbf{e}'$ is well-typed, if $\exists \mathbf{C}'$ such that $\emptyset; \Sigma \vdash \mathbf{e}' : \mathbf{C}'$, and $\emptyset; \Sigma \vdash \mathbf{e} : \mathbf{C}$ implies $\mathbf{C}' \preceq \mathbf{C}$, $\text{visible}(\mathbf{e}, \ell)$ is true (defined below), and $\overline{\ell \mathbf{e}}. \ell E[\mathbf{e}]$ is well-typed. Given Σ , the call stack $\ell \mathbf{e}$ is well-typed if $\exists \mathbf{C}$ such that $\emptyset; \Sigma \vdash \mathbf{e} : \mathbf{C}$ and $\text{visible}(\mathbf{e}, \ell)$ is true.

Given Σ , the constraint $\text{visible}(\mathbf{e}, \ell)$ is true if either $\mathbf{e} = \ell$ or, $\Sigma(\ell) = \mathbf{C}_0$ and $\emptyset; \Sigma \vdash \mathbf{e} : \mathbf{C}$ imply $\text{visible}(\mathbf{C}, \mathbf{C}_0)$ and for each subexpression \mathbf{e}' of \mathbf{e} , $\text{visible}(\mathbf{e}, \ell)$ is true.

In Theorem 3, we prove that if a program is well-typed, then it will not get stuck. We model deallocation using the explicit *reset* expression, which clears the fields of a gate if the gate is not used by any threads. We prove in Theorem 4 that the objects allocated in the gate before the reset are no longer accessible afterward.

Lemma 1 *If σ, P is well-typed and $\sigma, P \Rightarrow \sigma', P'$, then σ', P' is well-typed.*

We say that a thread of the form $t[\ell_0 \mathbf{v}]$ in P is terminated.

Lemma 2 *If σ, P is well-typed, then either all threads in P*

are terminated or there exists σ', P' such that $\sigma, P \Rightarrow \sigma', P'$.

We say that an irreducible program σ, P is stuck if P contains a non-terminated thread. As usual, \Rightarrow^* is the transitive and reflexive closure of \Rightarrow .

Theorem 3 *If σ, P is well-typed and $\sigma, P \Rightarrow^* \sigma', P'$, then σ', P' is not stuck and it is well-typed.*

Theorem 4 shows that in a well-typed program σ, P , if a gate $\sigma(\ell_0)$ is reset successfully, then the objects allocated in the scope represented by $\sigma(\ell_0)$ are not reachable in σ, P . Recall that if a scope represented by $\sigma(\ell_0)$ is not used by any thread, then $\text{refcount}(\sigma, \ell_0) = 0$. We say that ℓ is reachable in σ, P if either it is referenced in a thread of P or it is in the field of $\sigma(\ell')$, where ℓ' is reachable in σ, P .

Theorem 4 *If $P = P'' \mid t[\overline{\ell \mathbf{e}}. \ell E[\text{reset } \ell_0]]$, σ, P is well-typed, $\text{refcount}(\sigma, \ell_0) = 0$, and $\sigma, P \Rightarrow \sigma', P'$, then the objects of σ that are allocated in the scope represented by the gate $\sigma(\ell_0)$ are not reachable in σ', P' .*

V. RELATED WORK

The dangers involved in the RTSJ programming model have motivated Kwon *et al.* to propose a restricted programming model called Ravenscar-Java [12]. In Ravenscar memory areas cannot be nested and are single threaded. Scoped Types are intended to relax some of the restrictions of Ravenscar while remaining easy to understand and to verify. Boyapati [13] combine region-based memory management with ownership types to statically guarantee that real-time threads do not interfere with GC. While more flexible than Scoped Types, this approach is more invasive, requiring more program annotations, and more complex overall. Cyclone [14] is a type-safe language derived from C and it supports region-based memory management. Cyclone includes dynamic regions with lexically scoped lifetimes, stack and a heap region. To prevent dereferencing dangling pointers, Cyclone uses types parameterized by region names to track pointers to regions and uses effect annotations to prevent unsafe access to regions. The regions in Cyclone are limited to single threaded execution model. Also, the use of effects may not work with real-time Java, since the Java's type safety requirement does not allow objects to hold invalid references even if never used. Grossman extended Cyclone with a type system for preventing data races [15]. The MLKit is an implementation of ML which uses regions and region-inference [16], [17]. One of the main difference with the model presented here is that ML is a functional language without built-in support for concurrency.

There are two other open source virtual machines that implement parts of the RTSJ: Flex [18] and JRate [19], [5], as well as a number of commercial products and alternative proposals [20], [21], [22], [23], [24].

VI. CONCLUSION

In this paper we have introduced Scoped Types, a static programming discipline to support the kind of scoped memory management found in the Real-Time Specification for Java. The key contribution of our proposal is that it statically maintains the invariants that the RTSJ checks dynamically, yet imposes minimal syntactic overheads upon programmers. In particular, by statically associating scoped objects to their defining packages, Scoped Types ensure that illegal incoming references are never created, eliminating the potential for run-time errors caused by illegal assignment operations. By modeling nested scopes with nested packages, Scoped Types ensure that scopes will never form cycles, again eliminating the potential for runtime exceptions. By statically tracking the objects contained within each scope, Scoped Types ensure that it is safe to discard all the objects in a scope once the last thread has left it. We have formalized Scoped Types within the SJ-calculus and demonstrated that it avoids dangling references and cycles of scopes. We hope the techniques embodied within Scoped Types may be useful in many RTSJ applications, making real-time Java programming more practical, convenient and reliable.

For future work, we plan to relax the type constraints in SJ so that more programs can be written. For example, the current type rules prevent a scoped class from invoking methods inherited from parent packages. Such invocation can result in the implicit reference widening of the variable `this` in the method call and consequently, objects in the outer scope may hold references to the receiver object. To prevent such problems, we may require such methods to be *anonymous* [9] so that the variable `this` can only be used for field access and calls to other anonymous methods. Another limitation of Scoped Types is the lack of reuse of library classes. This problem may be addressed by introducing generics to Scoped Types similar to the Confined Generic Types in [9]. For example, a generic class `Vector<X>` may be used in a scoped package as containers for instances of the scoped class `C` if we instantiate the class as `Vector<C>`. Instances of the class `Vector<C>` can be allocated in the scope of the `C` objects and be confined in that scope and its nested scopes. Of course, we have to decide where to allocate the objects created in the vector class. For example, if the vector class is implemented using linked list, then we may allocate the linked list objects in the current scope as well but we must take care to protect the references to those objects from illegal access. An alternative is to allocate those objects in heap memory but this would make such vector class less usable in `NoHeapRealtimeThread`.

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